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In Accordance with E. O. 12958

on NOV 26 1997

CORONA/J-3 DESIGN REVIEW

PAGES E-1 thru E-50

(ELECTRICAL)

DECEMBER 1966

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# ELECTRICAL DESIGN

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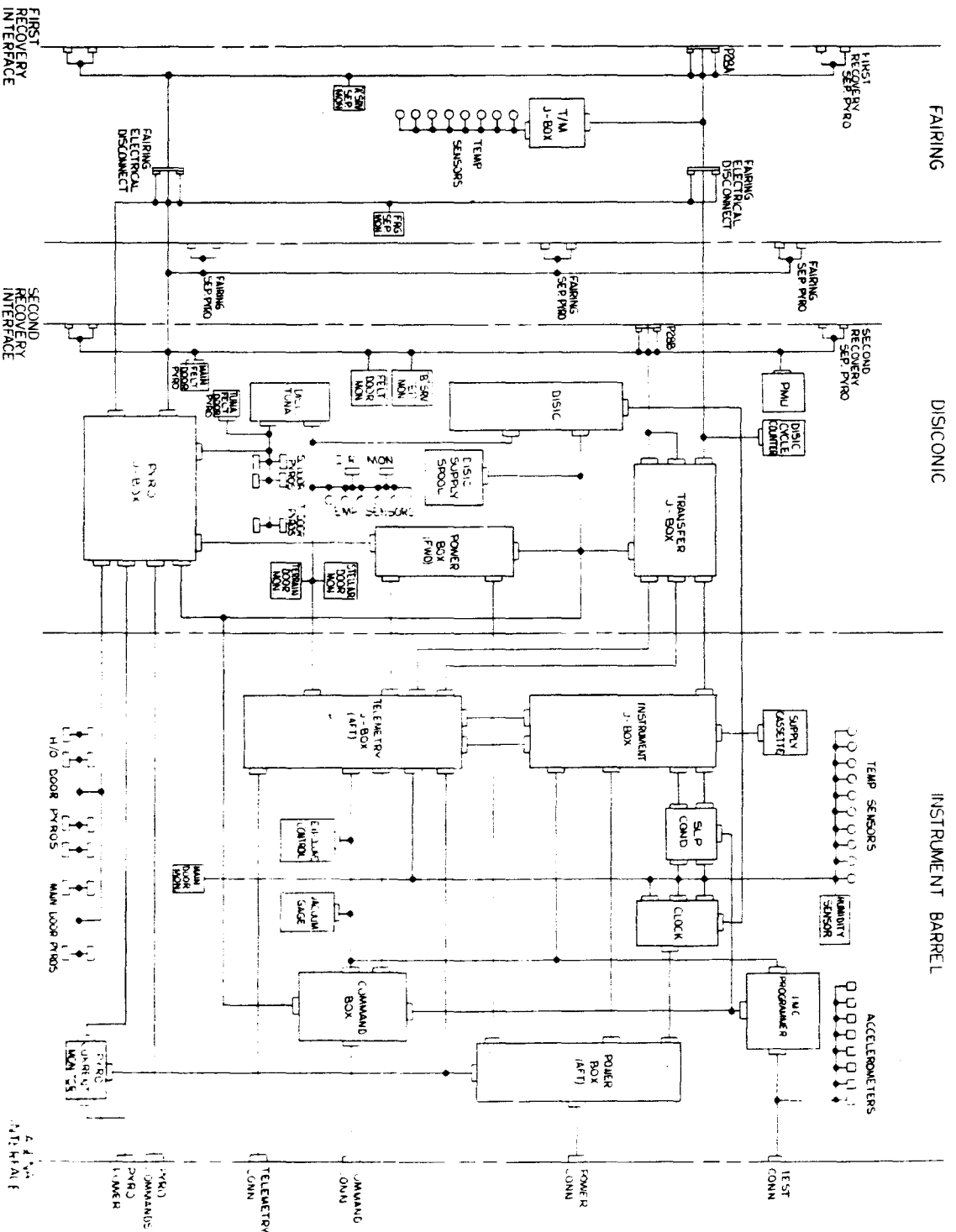
# ELECTRICAL DESIGN

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# ELECTRICAL DESIGN



INTERCONNECTION DIAGRAM

J-3 DESIGN REVIEW

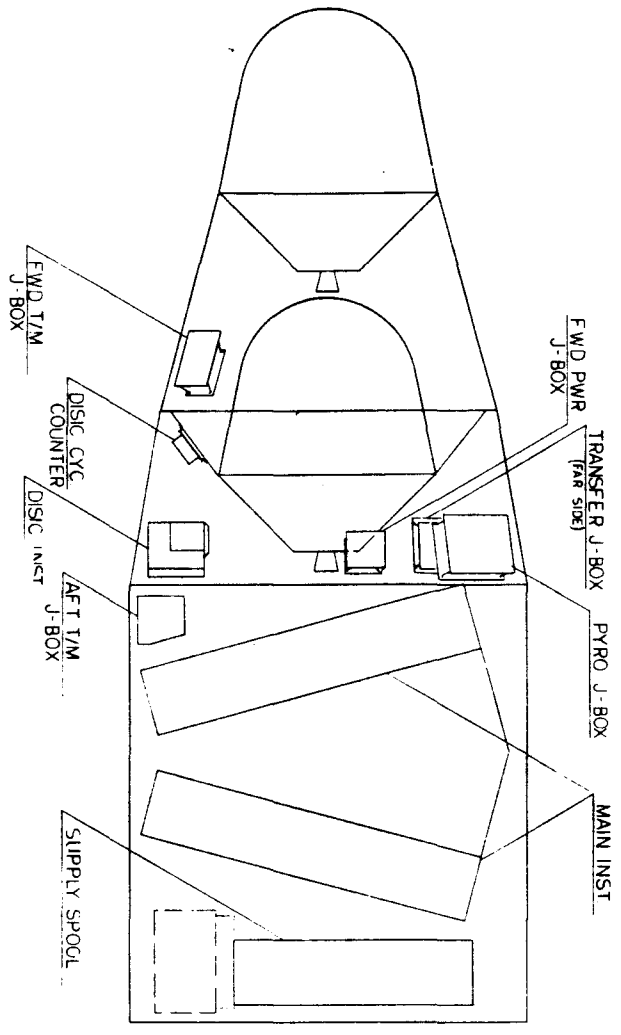
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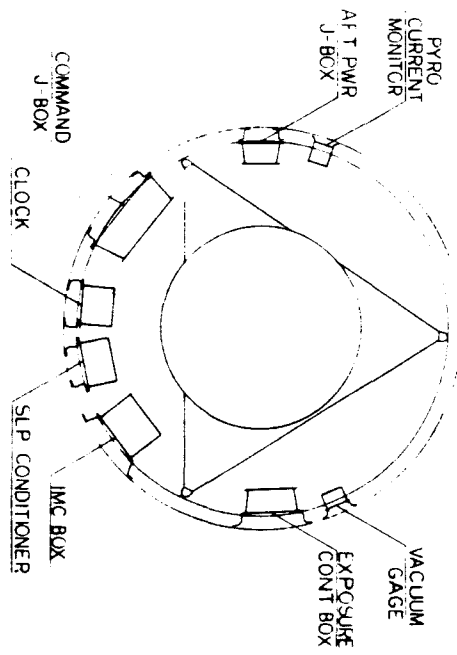
# ELECTRICAL DESIGN

## J-3 ELECTRICAL IMPROVEMENTS

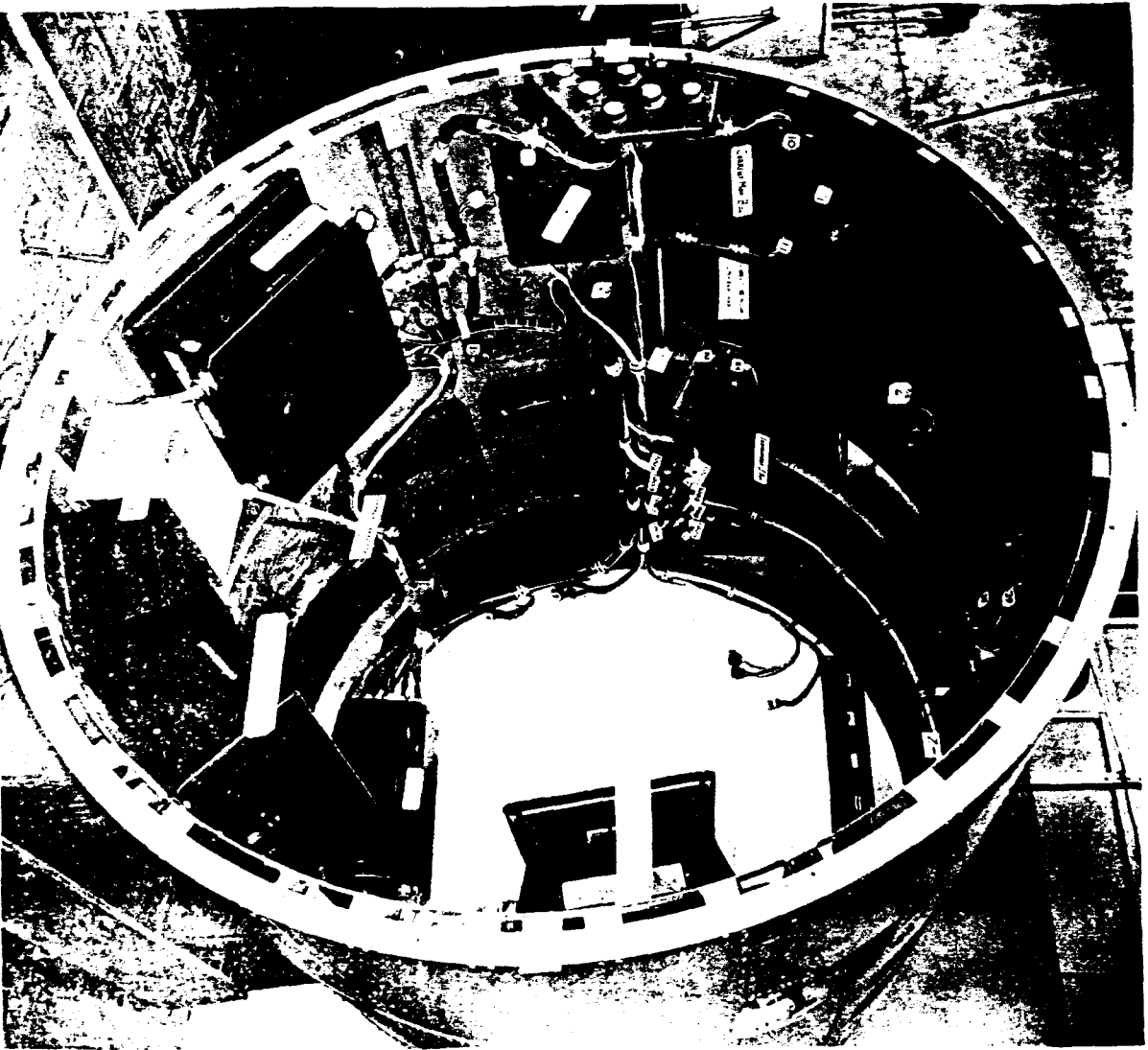
- COMMAND SYSTEM
  - MORE CAPABILITY FOR BYPASSING VARIOUS PORTIONS OF CAMERA OPERATE CIRCUITS.
  - ALL CAMERA OPERATE PROGRAMS ARE AVAILABLE FOR EMERGENCY OPERATE MODE.
  - FEWER SERIES ELEMENTS IN PRIMARY COMMAND AND CONTROL LINES INCREASES RELIABILITY.
  - EXPOSURE CONTROL FOR CR PAN CAMERA AND DISC PROVIDED.
  - RUGGEDIZED STEPPER SWITCHES ELIMINATE NEED FOR REMOVAL OF POWER TO SWITCHES DURING ASCENT.
  - REDUNDANT CIRCUITS PROVIDED FOR A TO B TRANSFER CONTROL CIRCUITS.
- IMC SYSTEM
  - EXPANDED SELECTION OF AVAILABLE CURVES TO MATCH ORBIT.
  - EARTH OBLATENESS CORRECTION FACTOR ADDED TO ORBIT ECCENTRICITY FUNCTION.
  - HAS SLEW TO HOME CAPABILITY IF START DELAY TIME PLUS FUNCTION PERIOD EXCEEDS ORBIT PERIOD.
- T/M SYSTEM
  - INCREASED BITS OF INFORMATION BY ADDING MORE COMMUTATED CHANNELS.
- PYRO SYSTEM
  - SEPARATE PYRO VOLTAGE BUSS IS PROVIDED.
  - EACH PYRO DEVICE IS ACTIVATED BY INDEPENDENT CIRCUITS FROM COMMAND TO SQUIB EXCITATION.
  - FUSISTOR LOCATED BETWEEN PYRO POWER SOURCE & RELAY CONTACTS PROTECTS BUSS FROM POSSIBLE PLASMA ARCING IN RELAYS.
- ADDED RECOVERABLE TAPE RECORDER.



EQUIPMENT INSTALLATION PROFILE

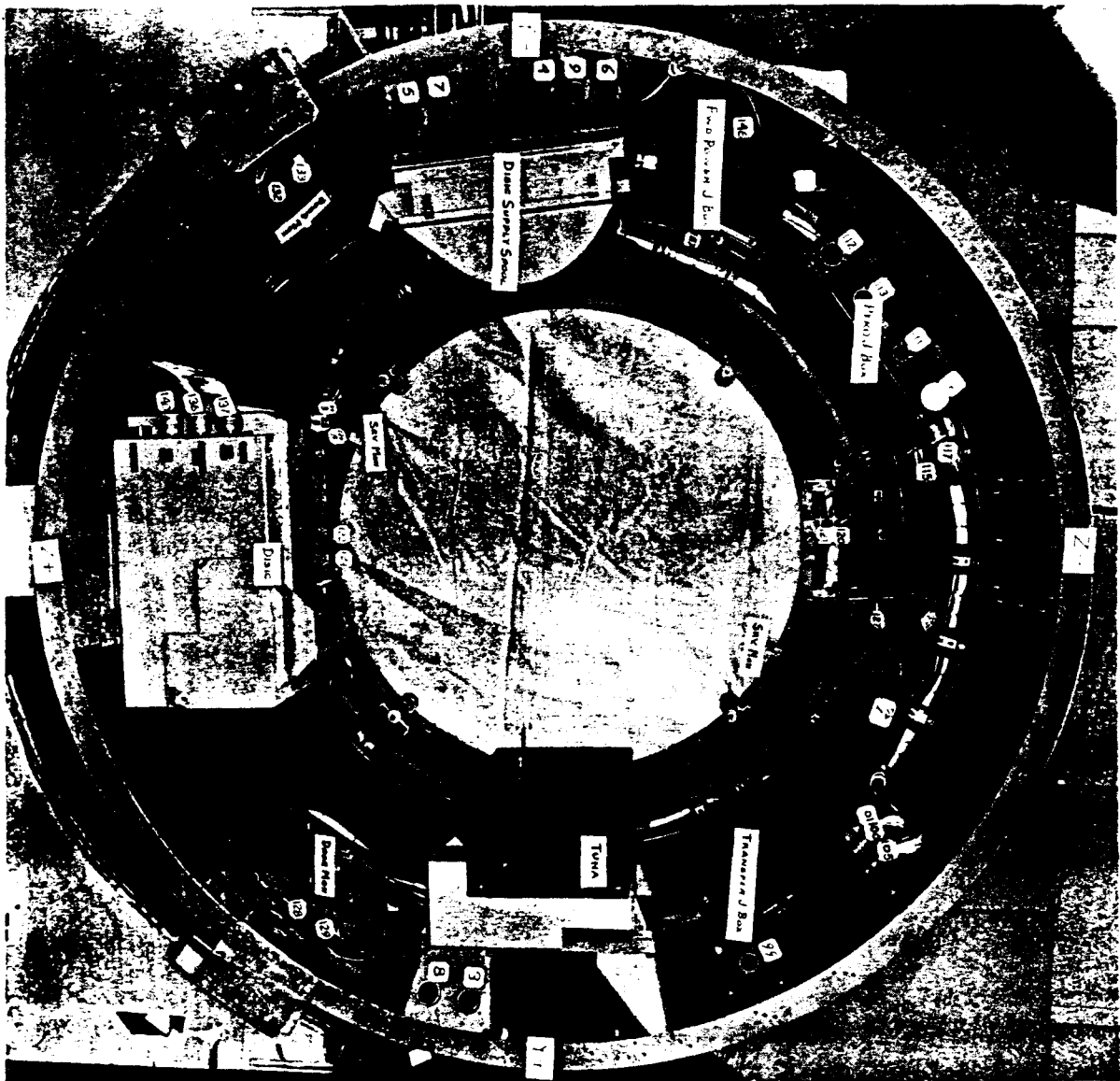


ELECTRICAL DESIGN



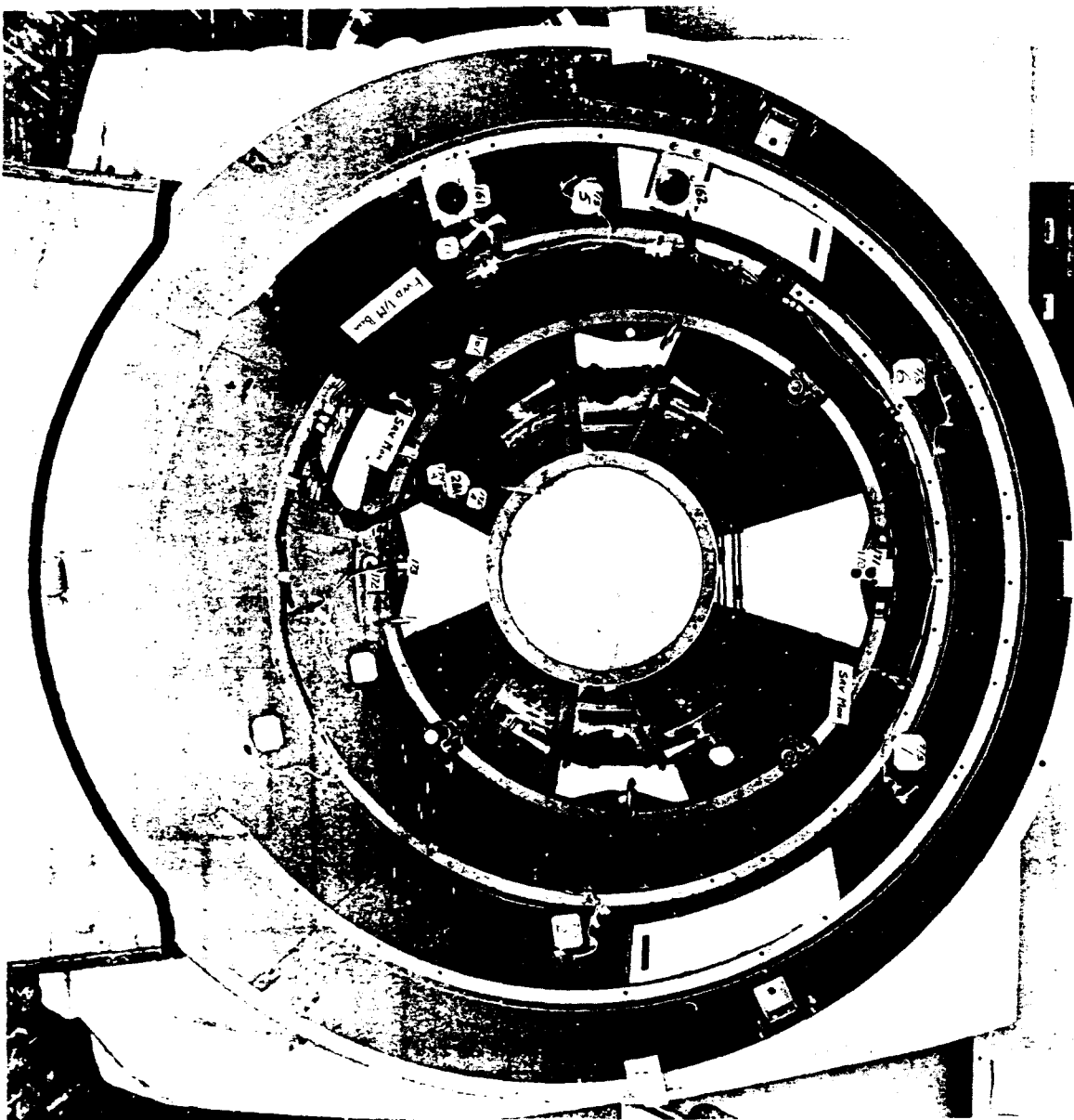
INSULATION BARREL, AG-15A ELECTRICAL  
(VIEW LOOKING FORWARD ENDS OF AG-15A BARREL, AG-15A)





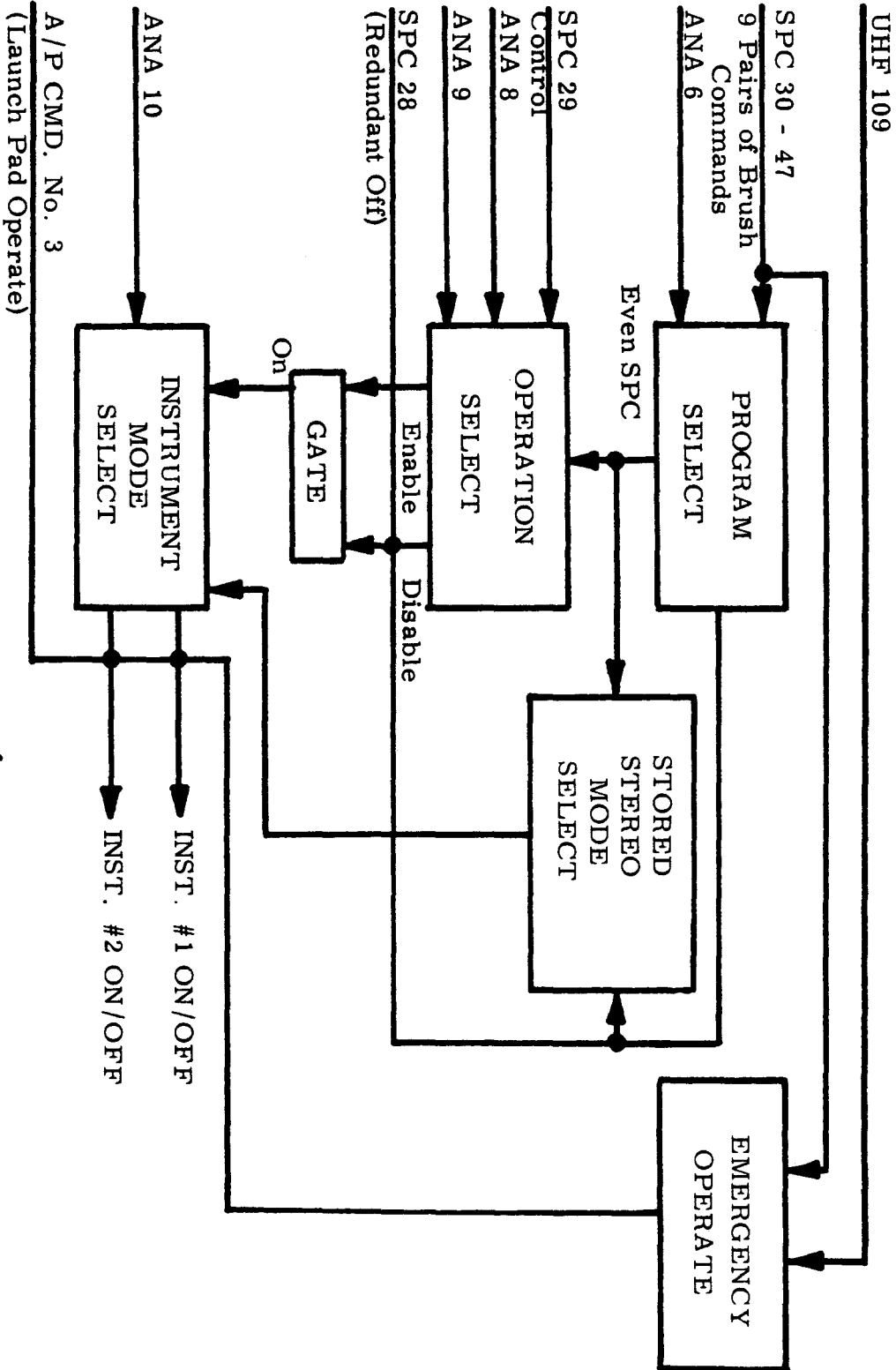
DISC CONIC/INSTRUMENT BARREL INTERFACE  
(VIEW 1:LOOKING FORWARD INTO DISC CONIC MOCK-UP)

ELECTRICAL DESIGN



FAIRING/DISK CONIC INTERFACE  
(VIEW LOOKING FORWARD INTO FAIRING MOCK-UP)

# ELECTRICAL DESIGN



CR PAN CAMERA CONTROL BLOCK DIAGRAM

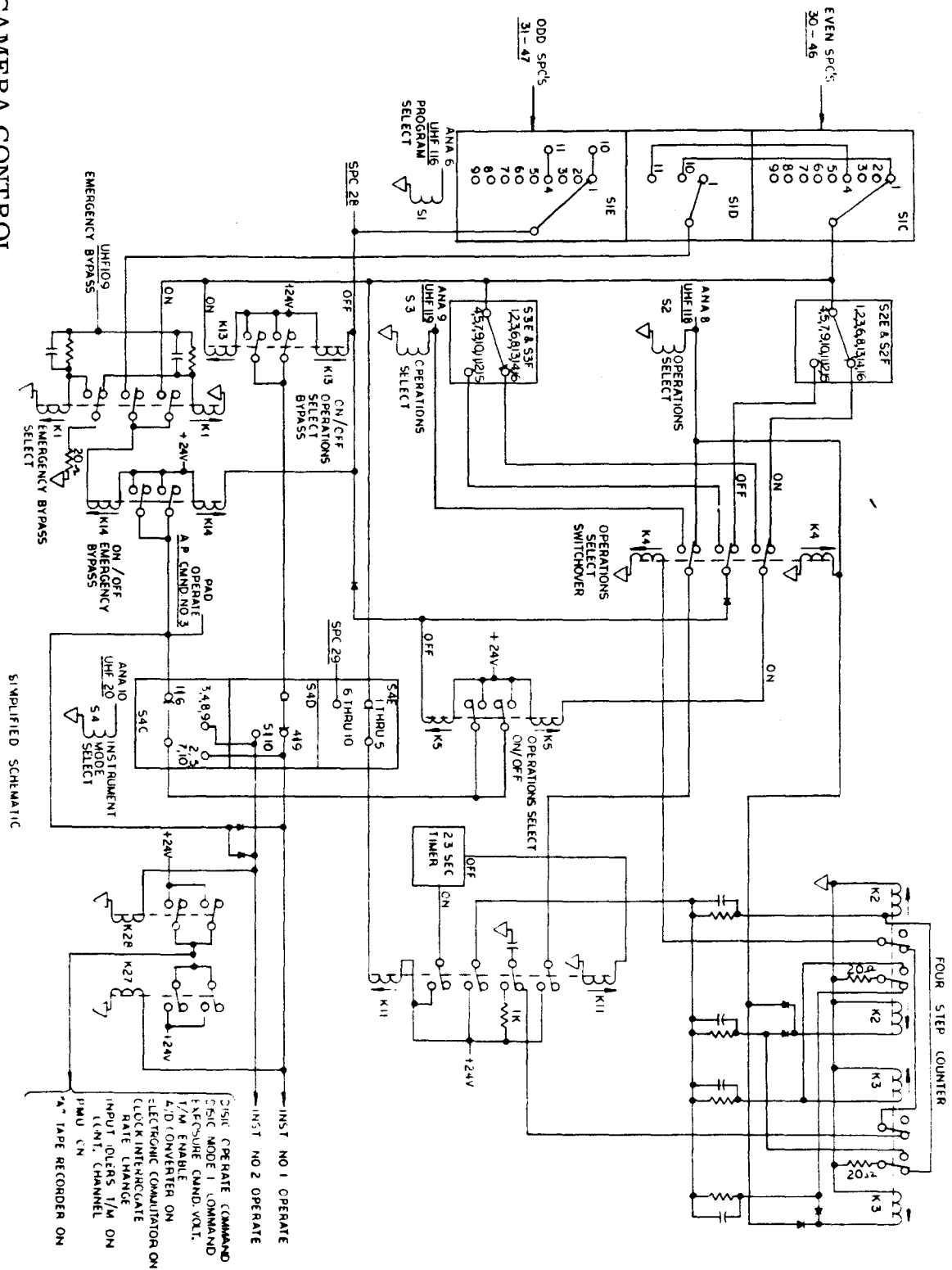
# ELECTRICAL DESIGN

## CR PAN CAMERA CONTROL

- UTILIZES TWO STEPPER SWITCHES FOR OPERATION SELECT CAPABILITY.
- OPERATION SELECT ALLOWS IN-FLIGHT EDITING OF SELECTED INSTRUMENT STORED PROGRAM COMMANDS.
- PROVIDES SELECTION OF 256 DIFFERENT COMBINATION SEQUENCES OF EIGHT SPC ENABLES OR DISABLES.
- NINE INSTRUMENT OPERATE PROGRAMS AVAILABLE.
- RTC CAPABILITY TO BYPASS PROGRAM SELECTOR, OPERATIONS SELECTOR, & INSTRUMENT MODE SELECTOR.

# ELECTRICAL DESIGN

## CR PAN CAMERA CONTROL



SIMPLIFIED SCHEMATIC

J-3 DESIGN REVIEW

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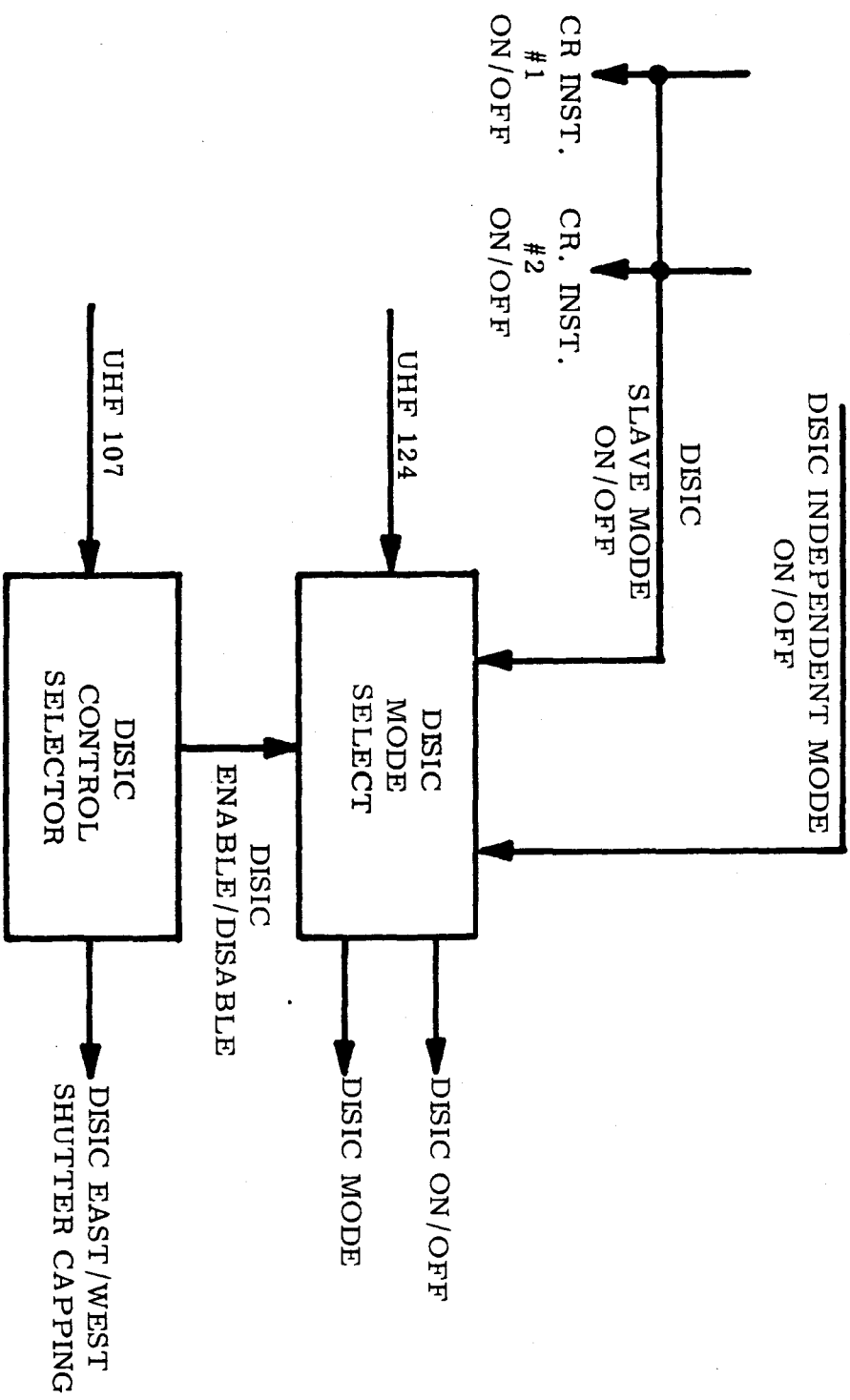
## ELECTRICAL DESIGN

### CR PAN CAMERA CONTROL COMMAND DESCRIPTION

- ANA-6/UHF-116 SELECTS ONE OF NINE AVAILABLE SPC PAIRS FOR CAMERA ON/OFF CONTROL. SUBSEQUENT IN-FLIGHT PROGRAM EDITING ACCOMPLISHED BY ANA-8/UHF-118 AND ANA-9/UHF-119.
- ANA-8/UHF-118 SELECTS ONE OF SIXTEEN AVAILABLE COMBINATIONS OF FOUR SEQUENTIAL STEPS TO PROVIDE ENABLING OR DISABLING OF THE SELECTED "ON" SPC.
- ANA-9/UHF-119 PROVIDES IDENTICAL CAPABILITY AS ANA-8/UHF-118. THIS SEQUENCE IS AUTOMATICALLY STARTED ON THE COMPLETION OF THE FOUR STEPS SELECTED BY ANA-8/UHF-118 THE RESULTING CAPABILITY IS 256 COMBINATIONAL SEQUENCES OF CAMERA ON/OFF COMMANDS.
- ANA-10/UHF-120 SELECTS EITHER, BOTH OR NEITHER INSTRUMENT TO RESPOND TO ON/OFF COMMANDS RESULTING FROM OPERATIONS SELECT. SELECTS SOURCE OF COMMAND WHICH ADVANCES OPERATIONS SELECT LOGIC. THE TWO SOURCES ARE THE SELECTED "ON" SPC OR SPC #29.
- UHF-109 PROVIDES CAPABILITY OF SELECTING EMERGENCY CAMERA OPERATIONS BYPASSING PROGRAM SELECT, OPERATION SELECT & INSTRUMENT MODE SELECT. IT IS ALSO USED AS A BACKUP RESET FOR TIMERS IN A TO B TRANSFER CIRCUITS.
- SPC 30 THROUGH 47: CAMERA PROGRAMS 1 THROUGH 9. EVEN NUMBERED SPC'S ARE ON COMMANDS, ODD NUMBERED SPC'S ARE OFF COMMANDS. EVEN NUMBERED SPC'S MAY BE CONVERTED TO AN ACTIVE "OFF" COMMAND BY OPERATIONS SELECT DISABLE LOGIC.
- SPC 28 CAMERA PROGRAMS REDUNDANT OFF COMMAND. ALL OF THE VARIOUS CAMERA OPERATE CONTROL CIRCUITS WILL RESPOND TO AN SPC 28.
- SPC 29 WHEN SELECTED BY ANA-10/UHF-120 ADVANCES THE ACTIVE OPERATIONS SELECT LOGIC SEQUENCE ONE STEP. PROVIDES BLOCK EDITING OF CAMERA OPERATE PROGRAMS.
- AP CMD #3 ALLOWS BLOCKHOUSE OPERATION OF CR INSTRUMENTS IN STEREO MODE.

ELECTRICAL DESIGN

SPC 48/49



DISIC CONTROL BLOCK DIAGRAM

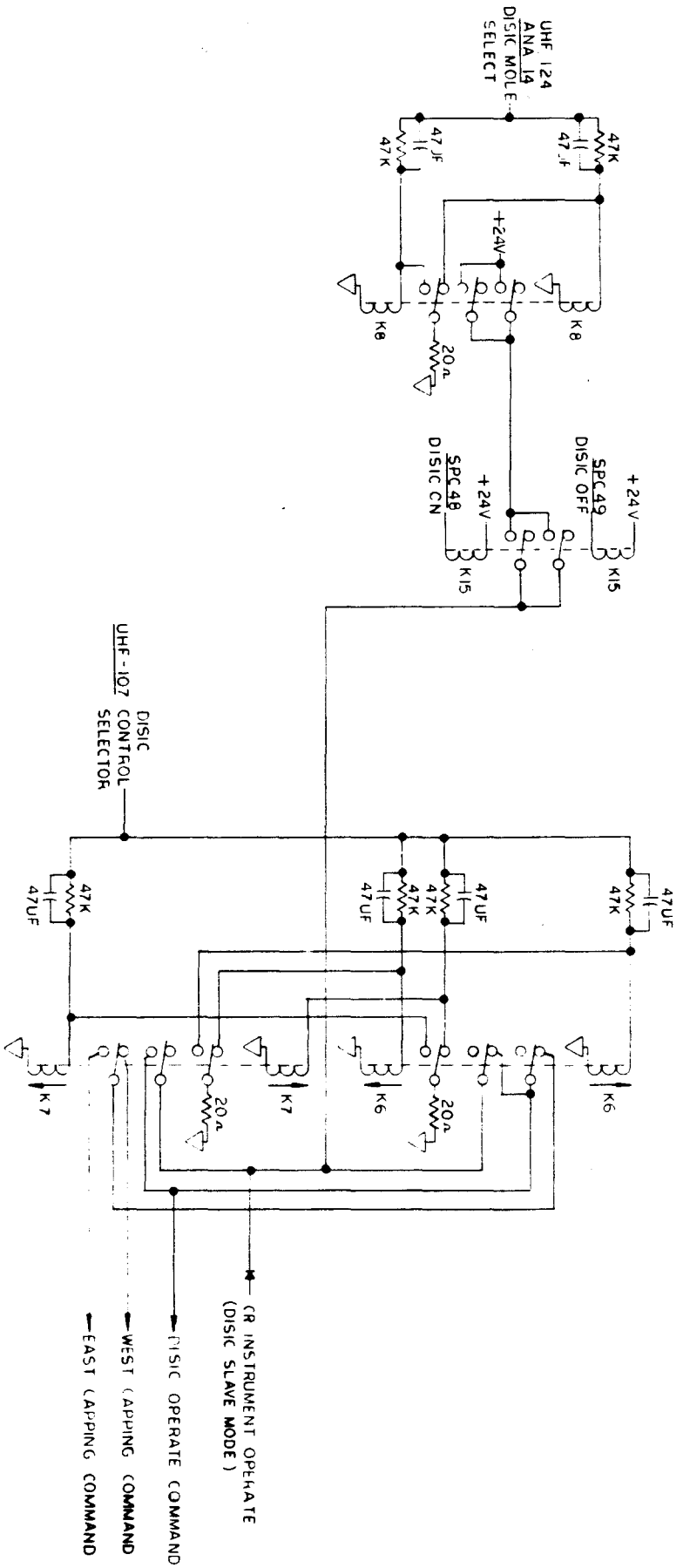
# ELECTRICAL DESIGN

## DISIC CONTROL

- HAS INDEPENDENT SPC CONTROL WHICH IS ENABLED BY RTC.
- SLAVE CONTROL MODE OVERRIDES INDEPENDENT CONTROL MODE
- IN SLAVE CONTROL MODE THREE PAIR OF STELLAR EXPOSURES ARE MADE TO ONE TERRAIN EXPOSURE.
- IN INDEPENDENT CONTROL MODE, ONE PAIR OF STELLAR EXPOSURES ARE MADE TO ONE TERRAIN EXPOSURE.
- RTC CONTROL IS USED TO INHIBIT UNCAPPING OF EITHER STELLAR SHUTTER
- RTC CONTROL ALSO PROVIDES COMPLETE SYSTEM DISABLING.



# ELECTRICAL DESIGN



DISIC CONTROL

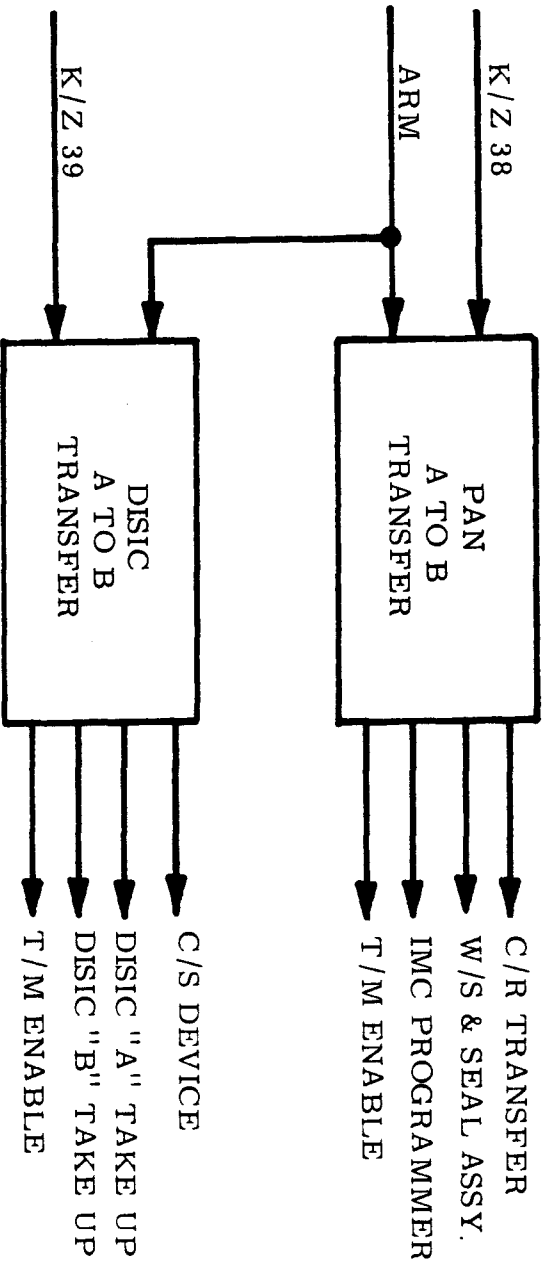
J-3 DESIGN REVIEW

# ELECTRICAL DESIGN

## DISIC CONTROL COMMAND DESCRIPTION

- ANA-14/UHF-124 ALTERNATELY DISABLES OR ENABLES SPC-48 & SPC-49 FOR USE AS AN INDEPENDENT DISIC OPERATE PROGRAM.
- UHF-107 PROVIDES INHIBITING CONTROL OF EITHER STELLAR UNCAPPING CIRCUITRY. IT IS ALSO USED TO DISABLE ANY OPERATE COMMAND FROM BEING SENT TO THE DISIC.

ELECTRICAL DESIGN



A TO B TRANSFER BLOCK DIAGRAM

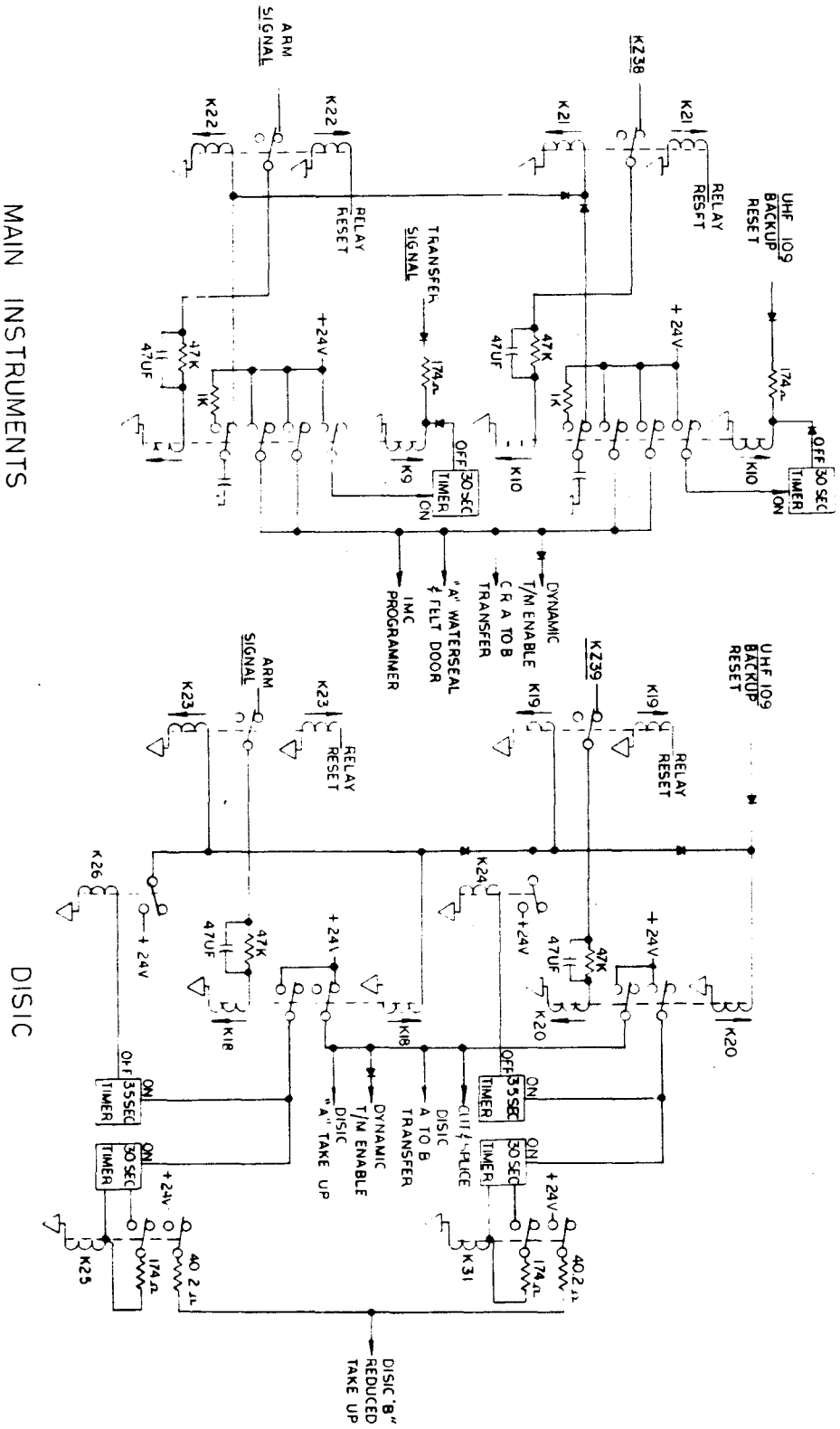
J-3 DESIGN REVIEW

ELECTRICAL DESIGN

A TO B TRANSFER

- INDEPENDENT EXECUTION OF DISIC OR CR "A" TO "B" TRANSFER SEQUENCES BY SECURE RTC'S.
- BOTH SEQUENCES BACKED-UP BY REDUNDANT CIRCUIT EXECUTED BY "A" RECOVERY COMMANDS.

# ELECTRICAL DESIGN



MAIN INSTRUMENTS

A TO B TRANSFER

DISIC

DISIC "B"  
REDUCED  
TAKE UP

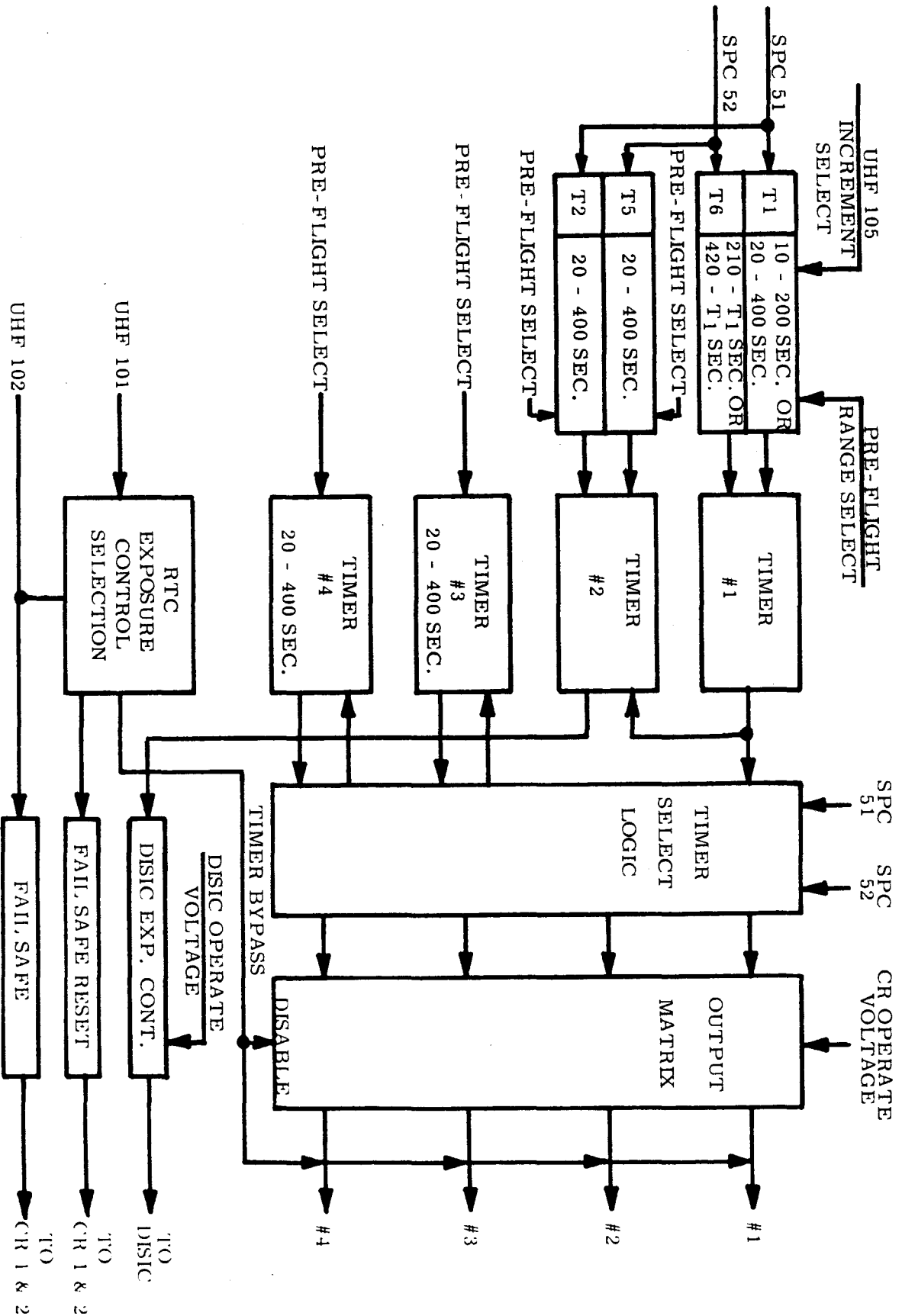
J-3 DESIGN REVIEW

# ELECTRICAL DESIGN

## A TO B TRANSFER COMMAND DESCRIPTION

- KZ-38 INITIATES A SEQUENCE OF EVENTS RESULTING IN THE TRANSFER OF THE CR INSTRUMENT'S FILM TAKE-UP CONTROL & INSTRUMENTATION FROM THE "A" SRV TO THE "B" SRV.
- KZ-39 INITIATES A SIMILAR SEQUENCE FOR THE DISC FILM TRANSFER.
- UHF-109: BACKUP RESET FOR TIMERS INITIATED BY KZ-38 & KZ-39.
- "A" RECOVERY ARM SIGNAL INITIATES IDENTICAL SEQUENCES TO THOSE INITIATED BY KZ-38 & KZ-39. IT RESULTS IN SIMULTANEOUS TRANSFER OF CR AND DISC FILM FROM SRV "A" TO "B" SRV, IF NOT PREVIOUSLY COMPLETED BY KZ-38 OR KZ-39.
- "A" RECOVERY TRANSFER SIGNAL: BACKUP RESET FOR TIMERS INITIATED BY THE ARM SIGNAL.

# ELECTRICAL DESIGN



EXPOSURE CONTROL PROGRAMMER BLOCK DIAGRAM

J-3 DESIGN REVIEW

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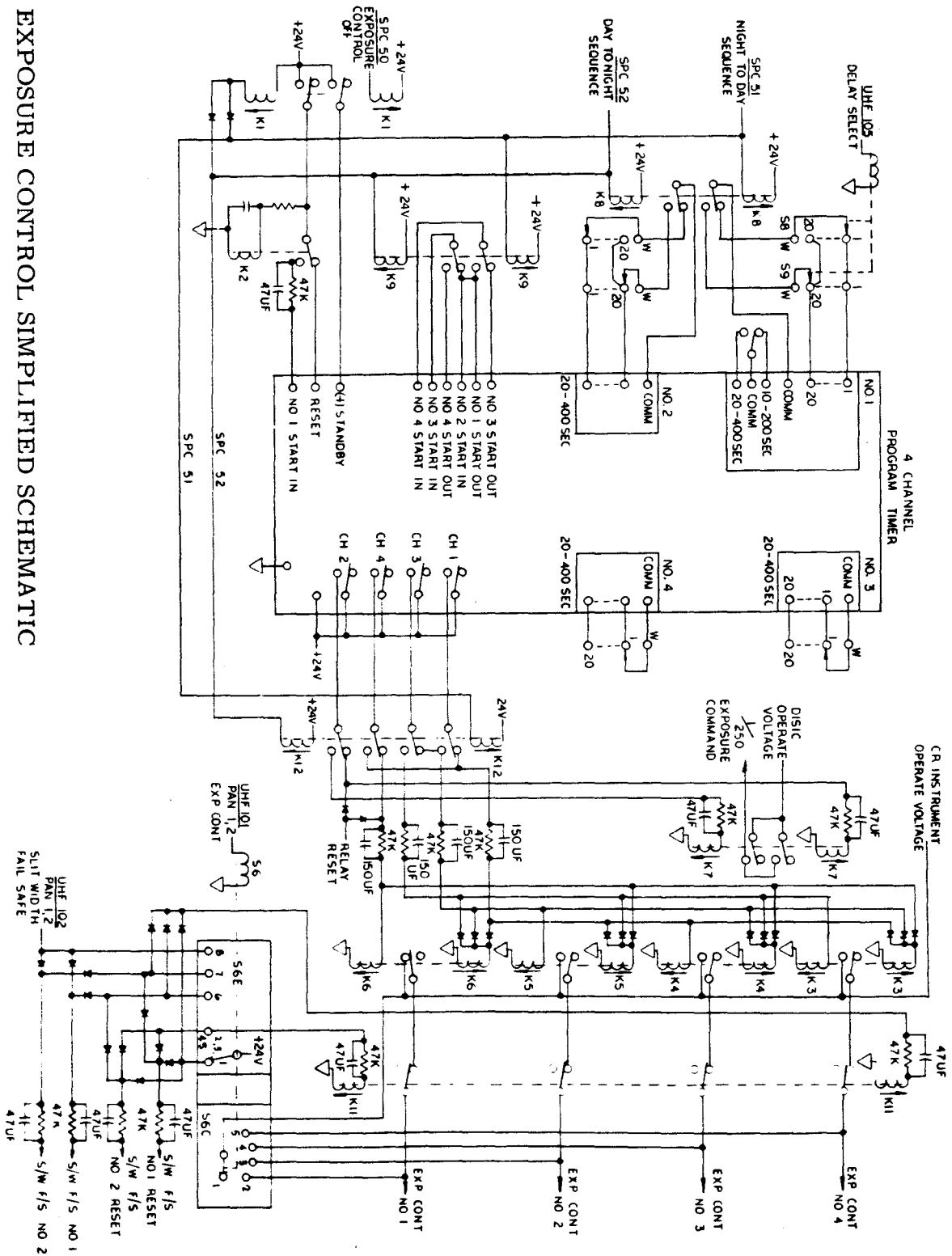
## ELECTRICAL DESIGN

### EXPOSURE CONTROL

- PROVIDES CONTROL OF FOUR EXPOSURE POSITIONS AND A FAIL SAFE POSITION FOR THE CR INSTRUMENT AND ONE EXPOSURE CHANGE FOR DISIC.
- SEQUENCES THROUGH EXPOSURE CHANGES FROM NIGHT TO DAY & DAY TO NIGHT POSITIONS.
- HAS RTC CONTROL FOR BYPASS OF SEQUENCER AND SELECTION OF ONE OF THE FOUR EXPOSURE POSITIONS FOR THE CR INSTRUMENTS FOR SPECIFIC TARGETING REQUIREMENTS.
- PROVIDES RTC CONTROL OF TIMING SYSTEM TO COMPENSATE FOR LAUNCH WINDOW UNCERTAINTIES AND/OR DELAYS.



# ELECTRICAL DESIGN



EXPOSURE CONTROL SIMPLIFIED SCHEMATIC

J-3 DESIGN REVIEW

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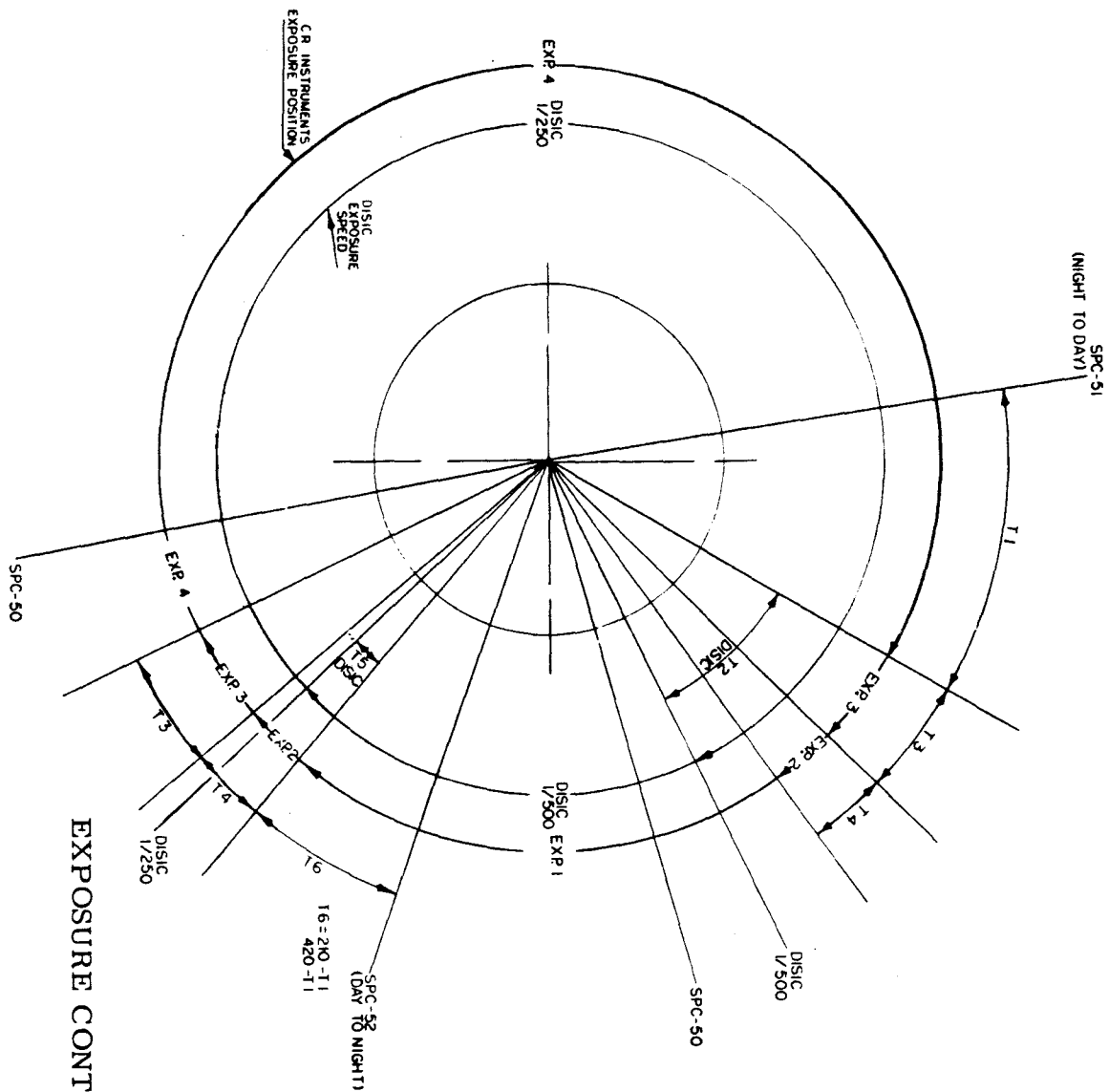
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## ELECTRICAL DESIGN

### EXPOSURE CONTROL COMMAND DESCRIPTION

- UHF-105 SELECTS ONE OF TWENTY AVAILABLE TIME DELAYS TO COMPENSATE FOR LAUNCH WINDOW UNCERTAINTIES AND/OR DELAYS.
- UHF-101 PROVIDES INDIVIDUAL SELECTION OF ANY ONE OF THE FOUR SLIT WIDTHS ON THE CR INSTRUMENTS AND INHIBITS NORMAL SEQUENCING. IT ALSO PROVIDES INDIVIDUAL FAIL SAFE RESET OF EITHER INSTRUMENT'S EXPOSURE CONTROL SYSTEM.
- UHF-102 BACKS UP UHF-101 FOR PLACING BOTH INSTRUMENTS IN THE EXPOSURE FAIL SAFE POSITION.
- SPC-51 INITIATES A NIGHT TO DAY SEQUENCE OF EXPOSURE SETTINGS FOR THE CR INSTRUMENTS AND THE DISC.
- SPC-52 INITIATES A DAY TO NIGHT SEQUENCE OF EXPOSURE SETTINGS FOR THE CR INSTRUMENTS AND THE DISC.
- SPC-50 REMOVES POWER FROM TIMERS BETWEEN SEQUENCES.

# ELECTRICAL DESIGN



EXPOSURE CONTROL TIMING DIAGRAM

J-3 DESIGN REVIEW

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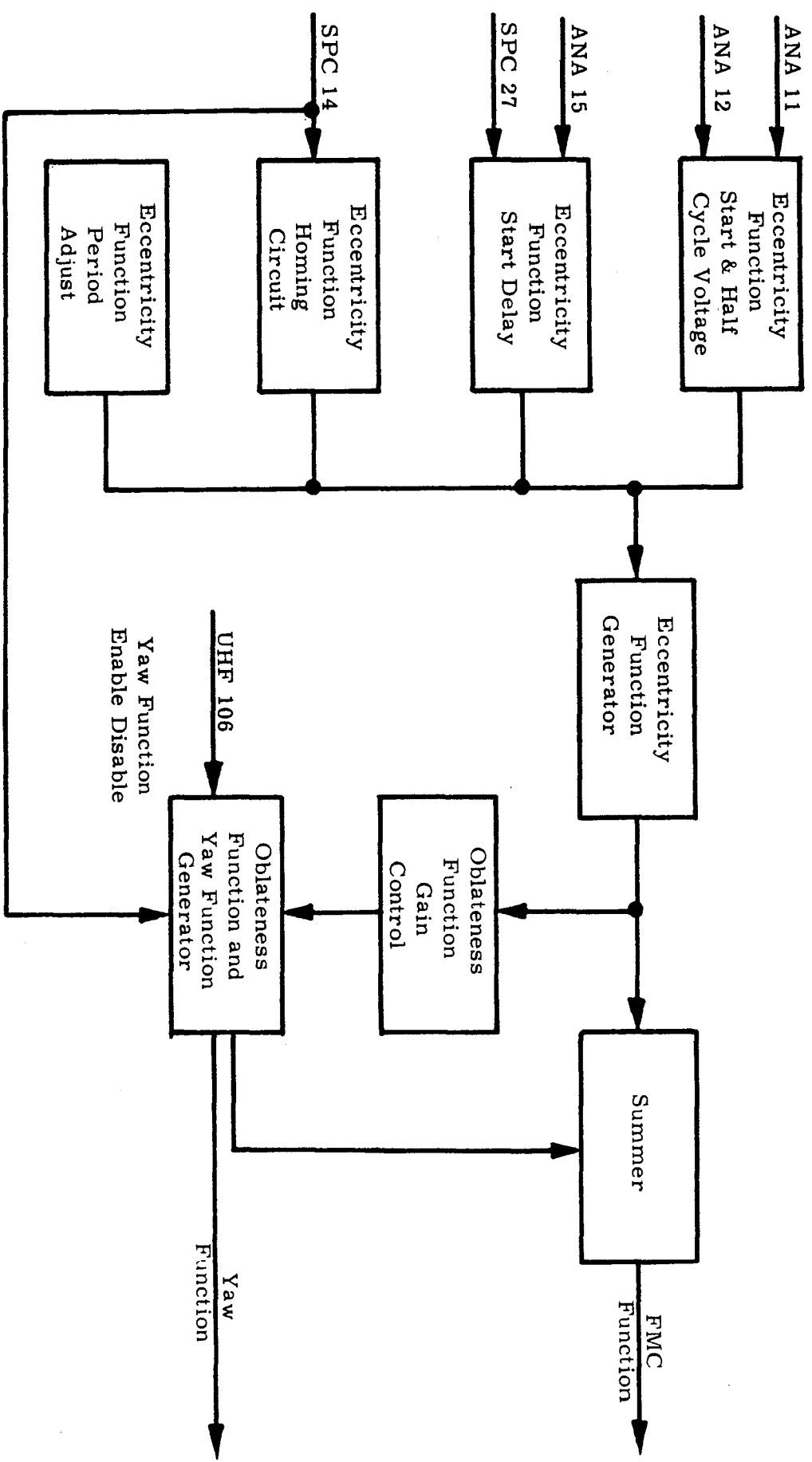
NO

ELECTRICAL DESIGN

EXPOSURE CONTROL

- T<sub>1</sub> SELECTABLE IN-FLIGHT WITH UHF 105
- T<sub>2</sub>, T<sub>3</sub>, T<sub>4</sub>, T<sub>5</sub>, T<sub>6</sub> SELECTED PRE-FLIGHT
  - EACH HAS A RANGE OF 20 TO 400 SECONDS IN 20 SECOND INCREMENTS
- EXP. 1 < EXP. 2 < EXP. 3 < EXP. 4

ELECTRICAL DESIGN



IMC PROGRAMMER BLOCK DIAGRAM

J-3 DESIGN REVIEW

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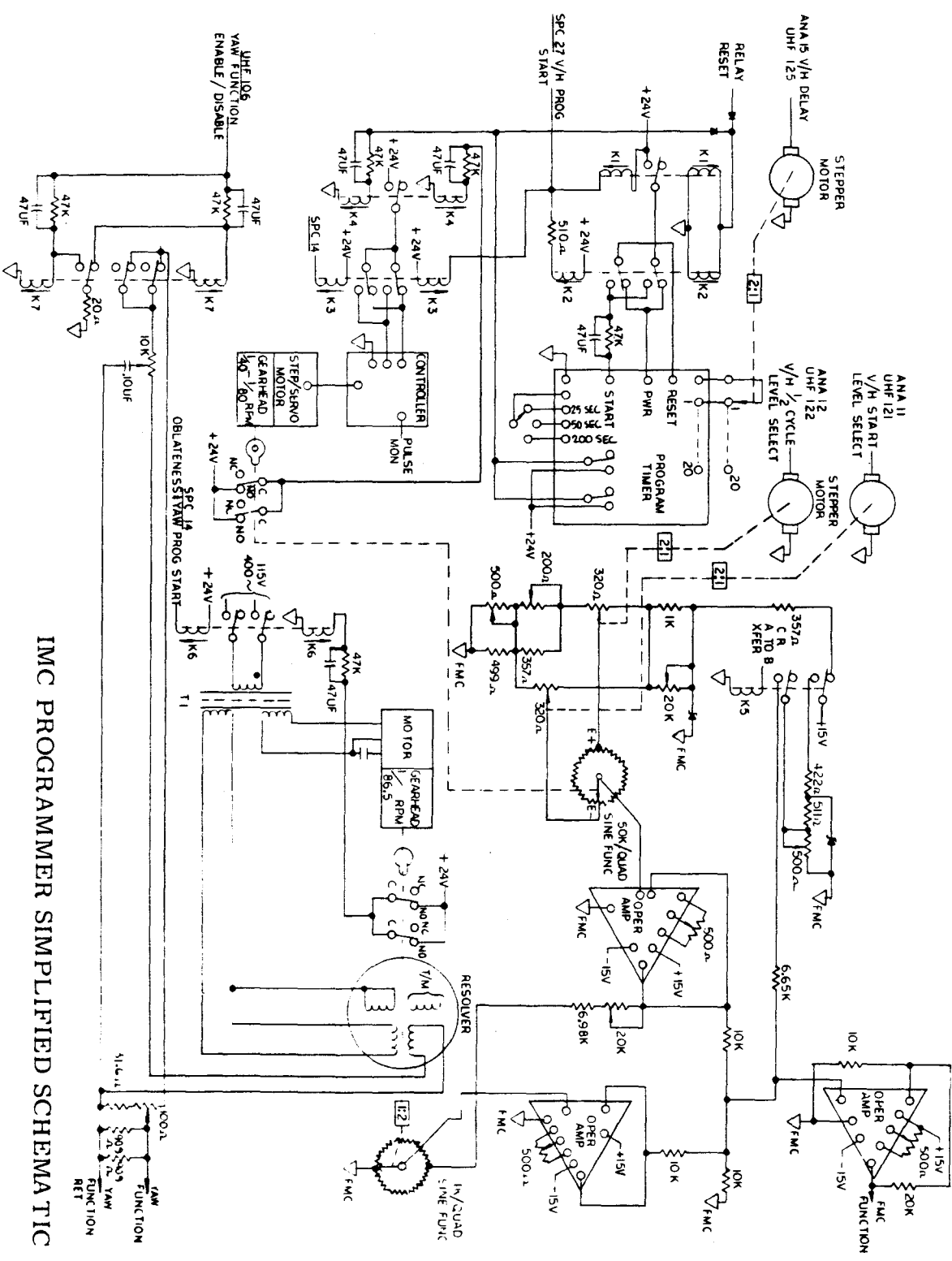
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# ELECTRICAL DESIGN

## IMC PROGRAMMER

- PROVIDES FMC FUNCTION TO CR INSTRUMENTS FOR CONTROL OF CYCLE & NODDING RATES.
- PROVIDES CROSS TRACK MOTION COMPENSATION IN FORM OF YAW ERROR SIGNAL TO AGENA GUIDANCE.
- FMC FUNCTION IS INSTANTANEOUS SUMMATION OF ORBIT ECCENTRICITY & EARTH OBLATENESS PARAMETERS.
- ECCENTRICITY FUNCTION HAS CAPABILITY FOR RTC SELECTION OF:
  - 20 START VOLTAGE LEVELS
  - 20 HALF CYCLE VOLTAGE LEVELS
  - 20 START DELAY TIMES
- OBLATENESS FUNCTION & YAW FUNCTION USE COMMON DRIVE SYSTEM INITIATED BY SPC.

# ELECTRICAL DESIGN



IMC PROGRAMMER SIMPLIFIED SCHEMATIC

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J-3 DESIGN REVIEW

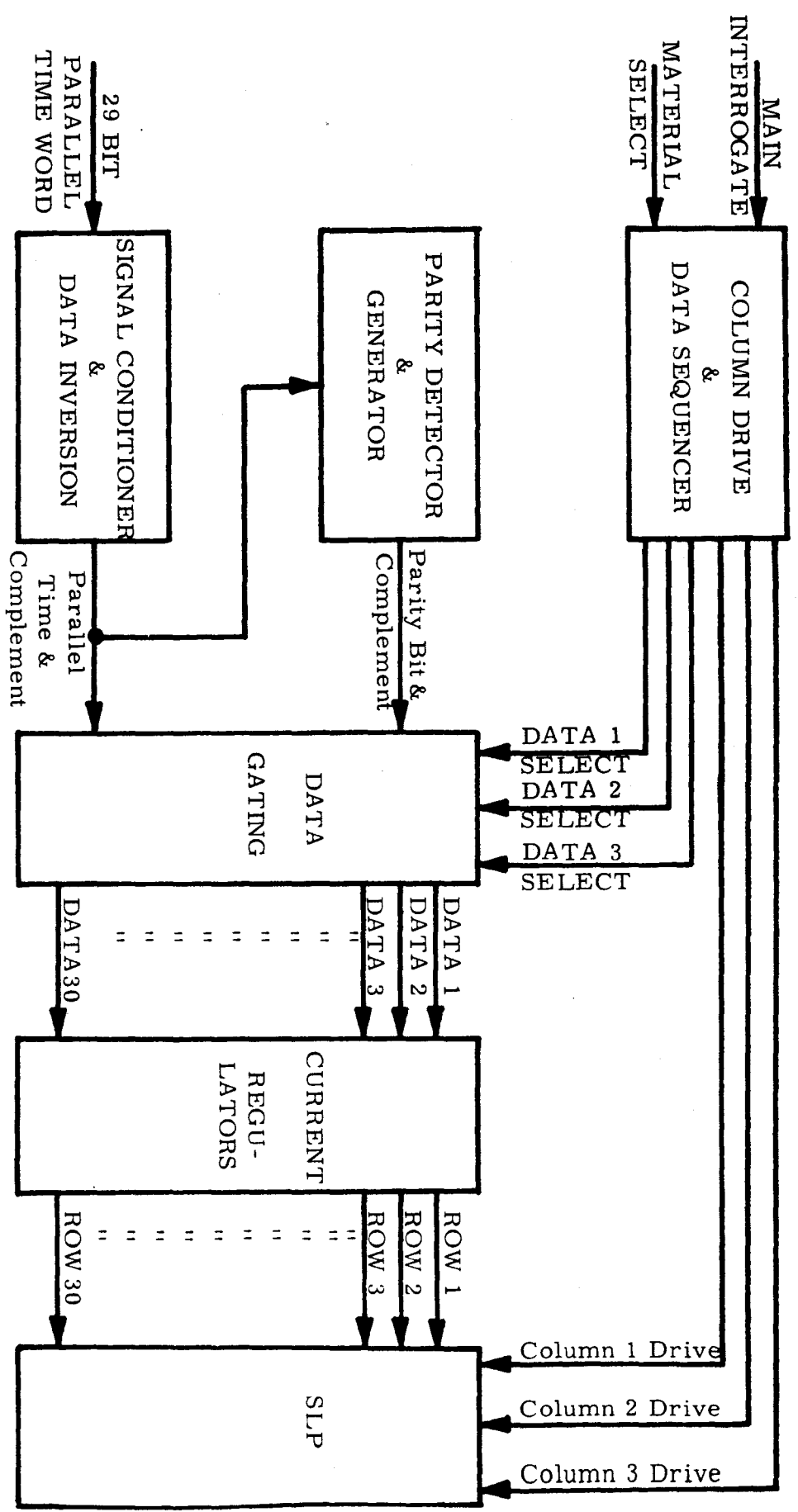
# ELECTRICAL DESIGN

## IMC PROGRAMMER COMMAND DESCRIPTION

- ANA-15/UHF-125 SELECTS ONE OF TWENTY AVAILABLE TIME DELAYS TO COMPENSATE FOR ORBIT UNCERTAINTIES. CONTROLS THE TIME BETWEEN RECEIPT OF A SPC #27 AND START OF THE ECCENTRICITY PORTION OF THE FMC FUNCTION.
- ANA-11/UHF-121 SELECTS ONE OF TWENTY AVAILABLE FMC FUNCTION START VOLTAGE LEVELS.
- ANA-12/UHF-122 SELECTS ONE OF TWENTY AVAILABLE FMC FUNCTION HALF CYCLE VOLTAGE LEVELS.
- UHF-106 ALTERNATELY ENABLES AND DISABLES OUTPUT OF YAW FUNCTION GENERATOR.
- SPC-27 INITIATES INFLIGHT VARIABLE TIME DELAY SYSTEM WHICH STARTS ECCENTRICITY FUNCTION GENERATOR.
- SPC-14 INITIATES START OF OBLATENESS FUNCTION GENERATOR AND YAW FUNCTION GENERATOR. IT IS ALSO USED TO SLEW THE ECCENTRICITY FUNCTION GENERATOR TO HOME POSITION.



# ELECTRICAL DESIGN



SLP CONDITIONER BLOCK DIAGRAM

## J-3 DESIGN REVIEW

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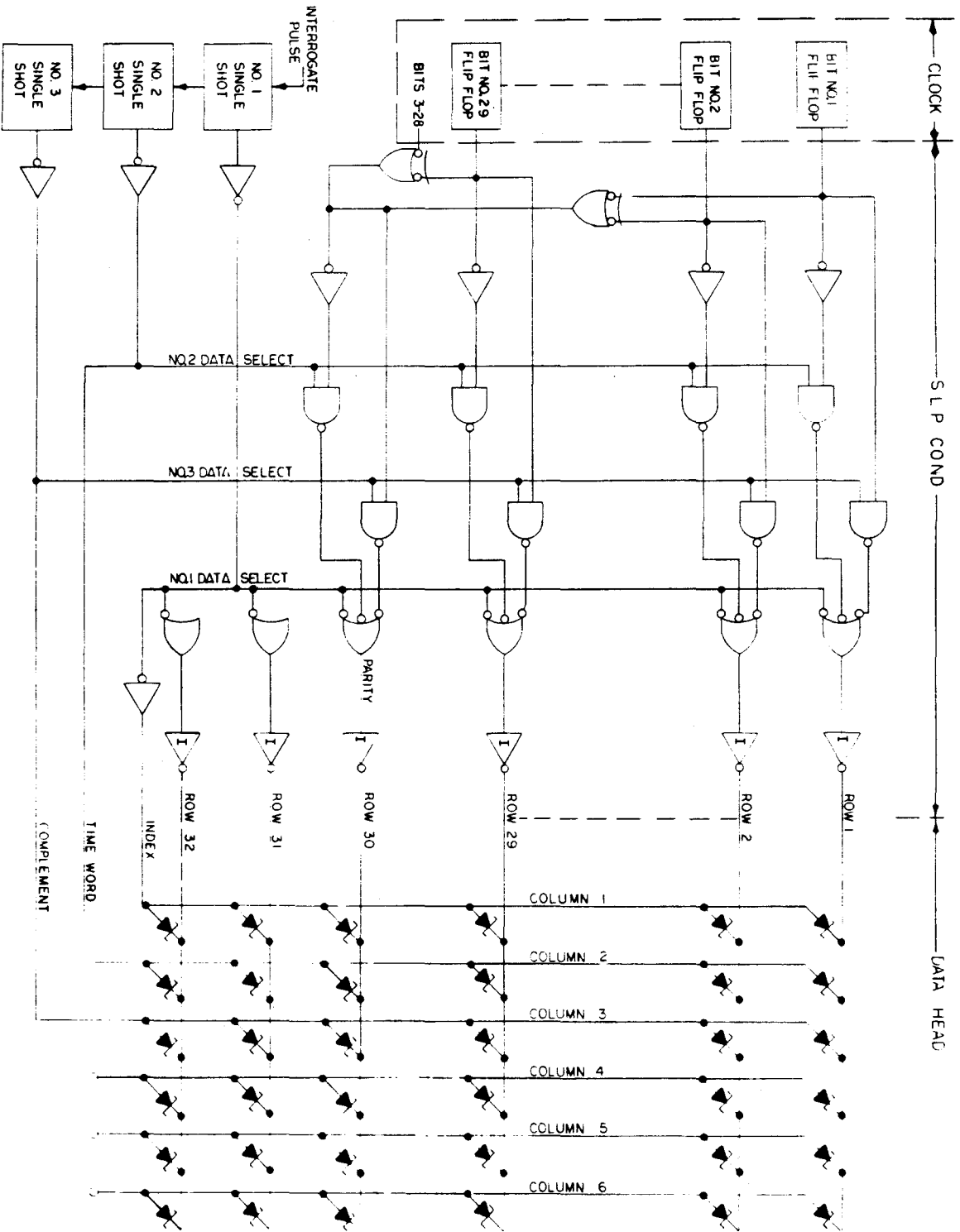
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## ELECTRICAL DESIGN

### SILICON LIGHT PULSER (SLP) CONDITIONER

- CONDITIONS & SEQUENCES CLOCK PARALLEL BINARY TIME WORD TO SLP DATA HEADS IN CR.
- PROVIDES 30 BIT INDEX, TIME WORD, COMPLEMENT OF TIME WORD & PARITY UTILIZING THREE COLUMNS OF DATA HEAD FOR EACH INSTRUMENT.
- HAS CAPABILITY FOR TWO DISTINCT VARIABLE COLUMN DRIVE DURATIONS TO PROVIDE PROPER DOT INTENSITY FOR VARIOUS FILM ASA SPEEDS.
- UTILIZES SOLID STATE MICRO LOGIC INTEGRATED CIRCUITS FOR GATING AND SEQUENCING.

# ELECTRICAL DESIGN



SILICONE LIGHT PULSER SYSTEM

J-3 DESIGN REVIEW

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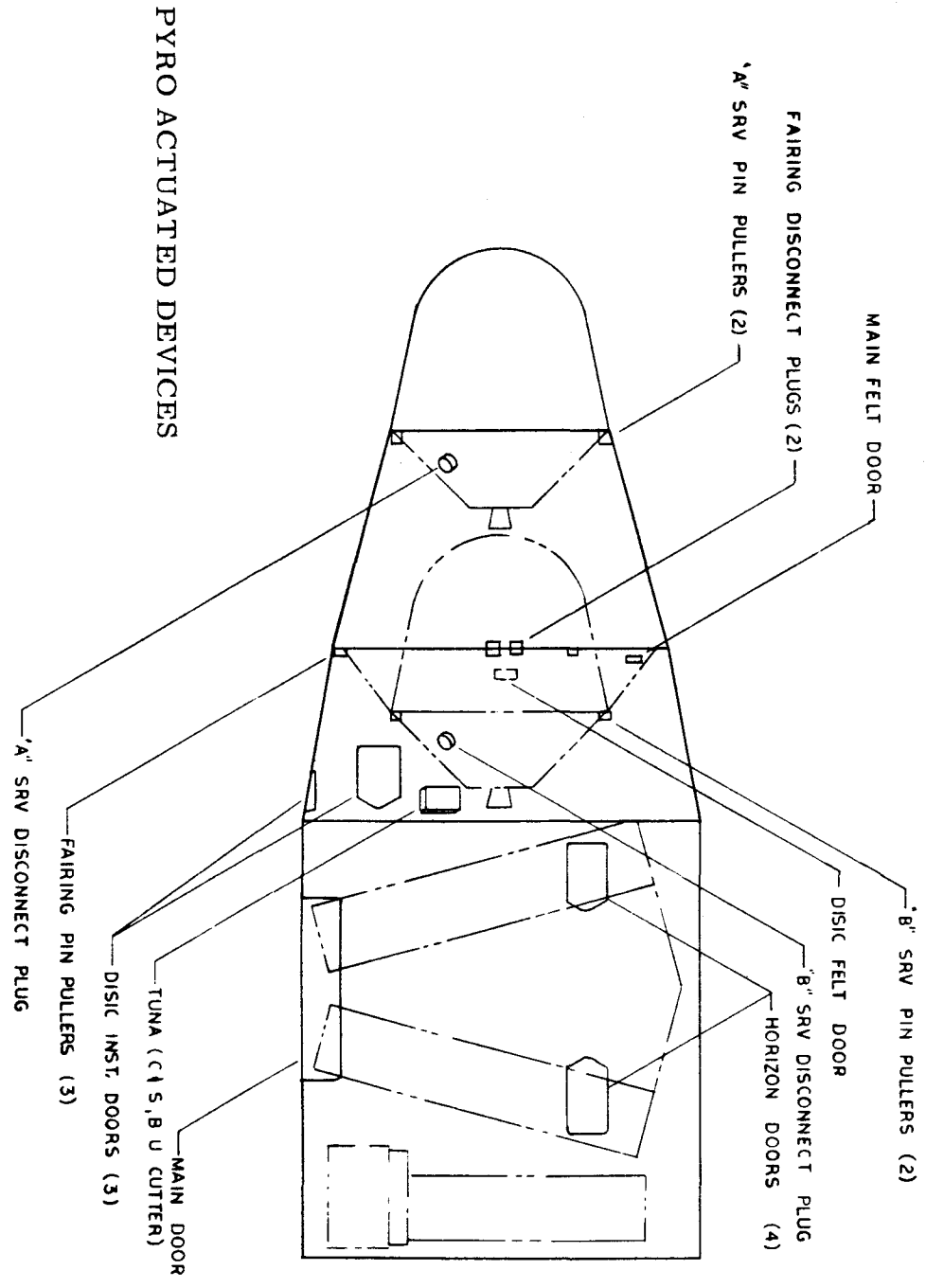
ELECTRICAL DESIGN

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J-3 DESIGN REVIEW

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PYRO ACTUATED DEVICES

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NO

ELECTRICAL DESIGN

PYRO SYSTEM

- ONE PLUG ON THE PAYLOAD/AGENA INTERFACE IS USED EXCLUSIVELY FOR SUPPLYING PYRO POWER AND PYRO RETURN TO THE PAYLOAD.
- EACH PYRO DEVICE IS ACTIVATED BY INDEPENDENT CIRCUITS FROM COMMAND TO SQUIB EXCITATION.
- A FUSISTOR PROTECTS EACH PYRO CIRCUIT.

# ELECTRICAL DESIGN

## PYRO EVENT & COMMAND DESCRIPTION

- IN-FLIGHT RESET (ORBIT MODE SIGNAL BACKUP)  
MAIN DOOR  
DISIC TERRAIN DOOR  
DISIC STELLAR #1 DOOR (R. H. LOOKING FORWARD)
- IN-FLIGHT RESET + 100 MILLISECONDS (ORBIT MODE SIG. BACKUP)  
LEFT HORIZON DOOR #1  
RIGHT HORIZON DOOR #1  
LEFT HORIZON DOOR #2  
RIGHT HORIZON DOOR #2  
DISIC STELLAR #2 DOOR (L. H. LOOKING FORWARD)
- KZ 38 (A TO B TRANSFER)  
MAIN SEAL ASSEMBLY (LIGHT SEAL)  
MAIN WATER SEAL
- KZ 39 CUT & SPLICE (DISIC)
- "A" ARM  
DISIC CUT & SPLICE (IF NOT PREVIOUSLY ACTUATED BY KZ 39)  
MAIN SEAL ASSEMBLY )  
"A" MAIN WATER SEAL ) IF NOT PREVIOUSLY ACTUATED BY KZ 38  
"A" T/M BATTERY (SRV)  
"A" RECOVERY BATTERY (SRV)
- "A" TRANSFER  
DISIC SEAL ASSEMBLY (LIGHT SEAL)  
"A" THRUST CONE THERMAL BATTERIES (SRV)  
"A" DISIC WATER SEAL (SRV)
- "A" DISCONNECT  
SPIN OFF DISCONNECT P28A

J-3 DESIGN REVIEW

# ELECTRICAL DESIGN

## PYRO EVENT & COMMAND DESCRIPTION (CONT'D)

- "A" SEPARATE  
"A" SRV PIN PULLERS  
NOTE: TEN SECONDS AFTER REMOVAL OF THE SEPARATE SIGNAL THE RECOVERY SWITCH-OVER CIRCUIT TRANSFERS THE RECOVERY COMMANDS TO THE "B" SRV.
- "B" ARM  
FAIRING SPIN OFF DISCONNECT PLUGS  
"B" T/M BATTERY (SRV)  
"B" RECOVERY PROGRAMMER BATTERY (SRV)  
BACKUP CUTTER (DISIC)
- "B" ARM + 100 MILLISECONDS  
FAIRING SEPARATE PIN PULLERS  
"B" MAIN WATER SEAL
- "B" TRANSFER  
"B" DISIC WATER SEAL (SRV)  
"B" THRUST CONE THERMAL BATTERIES (SRV)
- "B" DISCONNECT  
SPIN OFF DISCONNECT P28B
- "B" SEPARATE  
"B" SRV PIN PULLERS



# ELECTRICAL DESIGN

## POWER ESTIMATE

● MAXIMUM AVERAGE POWER (ASSUMING ALL PAYLOAD SUBSYSTEMS ARE OPERATED AT THEIR AVERAGE CURRENT SIMULTANEOUSLY)

● UNREGULATED 24 VDC

	AMPS
CR INSTRUMENTS	20.00
DISIC	3.50
T/M SYSTEM	1.00
IMC SYSTEM	1.00
CLOCK	0.35
CLOCK CONDITIONER	0.50
A/P COMMAND SYSTEM	1.00

TOTAL  
(APPROX. 27.35  
30.0 AMPS)

● 115 V 400

CR INSTRUMENTS	0.50
IMC SYSTEM	<u>0.07</u>

TOTAL  
(APPROX. 0.57  
0.6 AMPS)

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J-3 DESIGN REVIEW

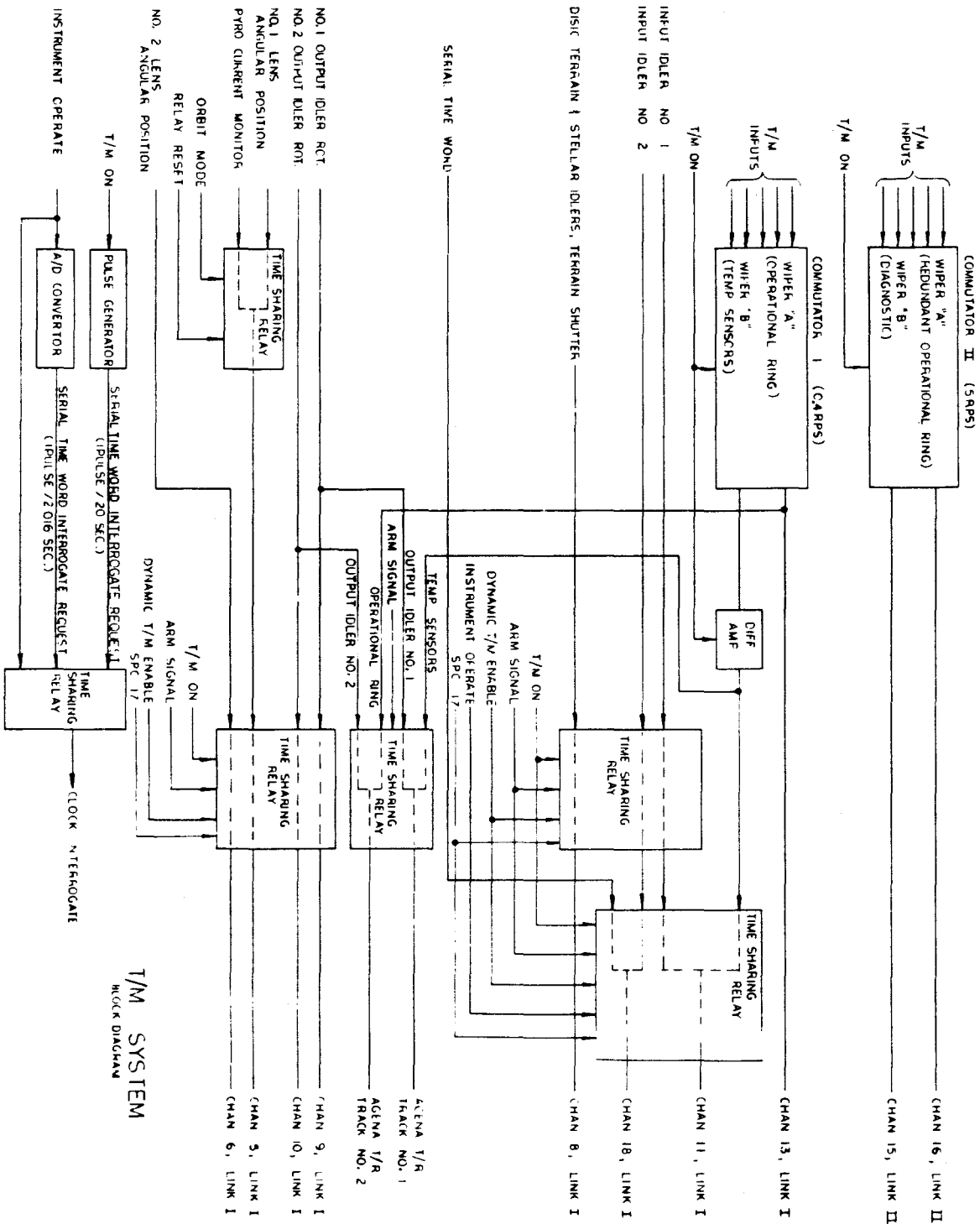
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# ELECTRICAL DESIGN

## POWER DISTRIBUTION PLAN

- A POWER BUSS IS SUPPLIED FOR EACH POWER SOURCE.
  - A HIGH CAPACITY POWER BUSS RUNS FROM THE AGENA INTERFACE TO THE FORWARD END OF THE PAYLOAD.
  - FEEDER CABLING IS PROVIDED FROM THE MAIN BUSS TO EACH EQUIPMENT IN THE PAYLOAD.
  - A COMPATIBLE POWER RETURN SYSTEM IS USED THROUGHOUT THE SYSTEM.
- POWER SUPPLIED FROM THE AGENA INCLUDES + 24 VDC UNREGULATED AND 115 V 400
  - 5 V DC-DC CONVERTERS ARE USED FOR TELEMETRY EXCITATION & SLP CONDITIONER.
  - A + 15 VDC CONVERTER IS USED IN THE IMC PROGRAMMER

# ELECTRICAL DESIGN



T/M SYSTEM  
BLOCK DIAGRAM

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J-3 DESIGN REVIEW

NO. [REDACTED] Page E-42

# ELECTRICAL DESIGN

## TELEMETRY SYSTEM

- PROVIDES INSTRUMENT OPERATIONAL DATA
  - TEMPERATURE
  - OPERATIONAL MODES
  - COMMAND MODES
  - CYCLES OF OPERATION
  - PAYLOAD CONSUMPTION
  - CLOCK SERIAL TIME WORD
- T/M CONTROL AND COMMAND DESCRIPTION
  - T/M ON (AGENA INTERFACE)
    - ENERGIZES ALL T/M CIRCUITS
    - RESETS CONTINUOUS T/M ENABLE
  - T/M DATA ENABLE
    - ENERGIZES ALL T/M CIRCUITS
  - INSTRUMENT OPERATE
    - ENERGIZES ALL T/M CIRCUITS
    - ENERGIZES RECOVERY TAPE RECORDER SYSTEM
  - DYNAMIC T/M ENABLE (SPC 17, KZ 38 & 39, ARM SIG.)
    - ENABLES CONTINUOUS T/M IF T/M ON IS PRESENT

# ELECTRICAL DESIGN

## T/M INSTRUMENTATION LIST

### SIX CONTINUOUS CHANNELS

- TERRAIN IDLER, TERRAIN SHUTTER & STELLAR IDLER
- PAN #1 OUTPUT IDLER ROTATION, 99/101 CLUTCH COMMAND
- PAN #1 LENS ANGULAR POSITION, CENTER OF FORMAT COMMAND, PYRO CURRENT MONITOR DURING ASCENT
- PAN #2 OUTPUT IDLER ROTATION, 99/101 CLUTCH COMMAND
- PAN #2 LENS ANGULAR POSITION, CENTER OF FORMAT COMMAND
- CLOCK SERIAL WORD (TIME SHARED WITH PAN #2 INPUT IDLER)

### FOUR COMMUTATED CHANNELS

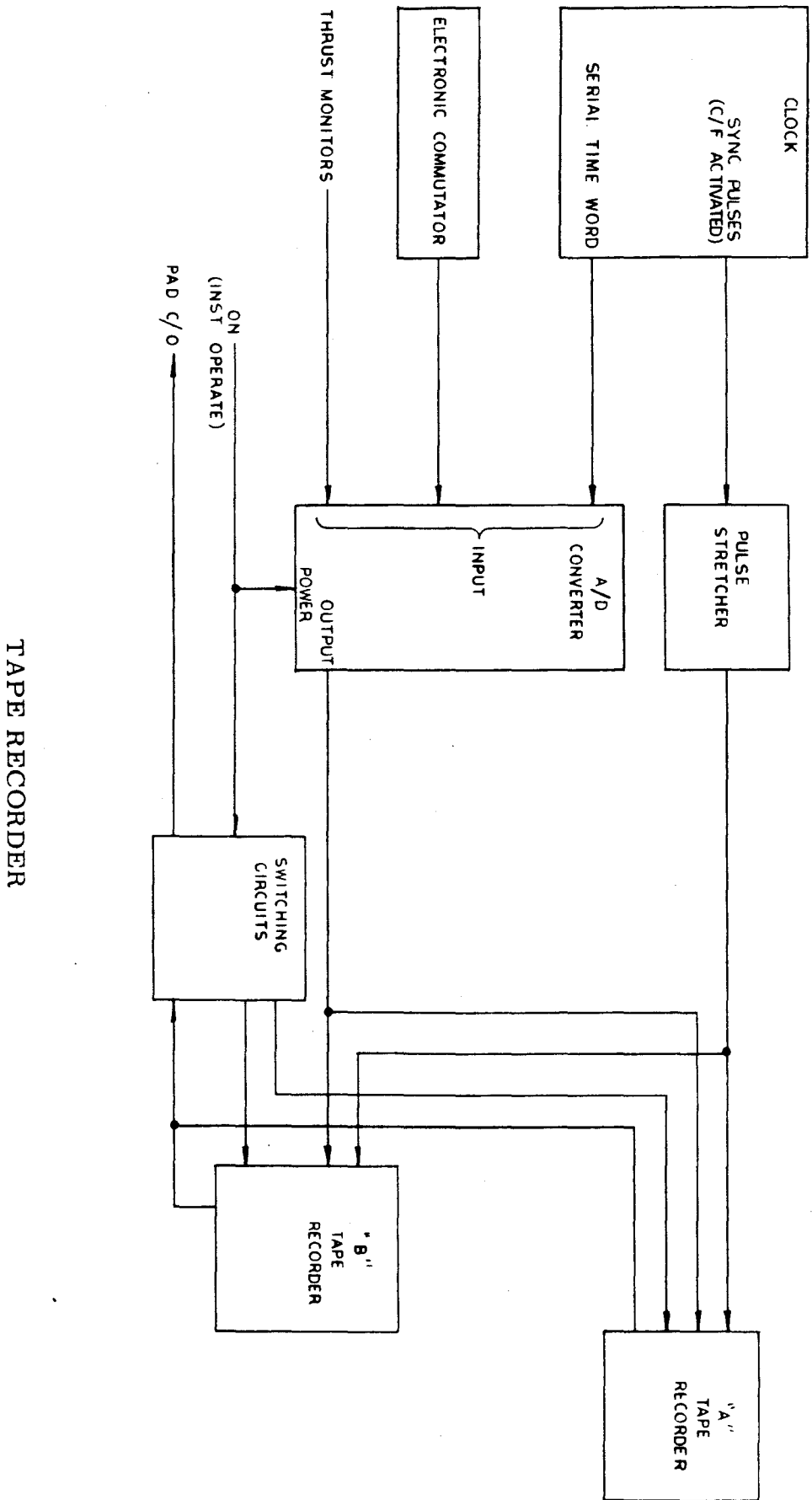
- TEMPERATURE MEASUREMENTS
  - TIME SHARED WITH PAN #1 INPUT IDLER
  - 0.4 RPS X 60 POINT COMMUTATOR RING B
    - 22 TEMP. SENSORS ON STRUCTURE & SKIN
    - 16 INSTRUMENT TEMP. SENSORS
    - 4 SRV TEMP. SENSORS
    - 2 DISC TEMP. SENSORS
    - 2 BLAST SHIELD TEMP. SENSORS
    - 1 TEMP. SENSOR ON IMC PROGRAMMER
    - 1 PMU TEMP. SENSOR
- OPERATIONAL CONTROL
  - 0.4 RPS X 60 POINT COMMUTATOR RING A
    - RTC & SPC STATUS MONITORS
    - PAN #1 & #2 CYCLE COUNTER MONITOR

# ELECTRICAL DESIGN

## T/M INSTRUMENTATION LIST (CONT'D)

- TERRAIN CYCLE COUNTER
- SRV SEPARATIONS
- FAIRING SEPARATIONS
- PAN #1 & #2 FILM CHANGE DETECTOR
- SRV RECOVERY BATTERY MONITOR
- STELLAR & TERRAIN TAKE-UP DIAMETER
- PAN #1 & PAN #2 TAKE-UP DIAMETER
- REDUNDANT OPERATIONAL CONTROL
  - 5.0 RPS X 60 POINT COMMUTATOR RING A
- DIAGNOSTIC MEASUREMENTS
  - 5.0 RPS X 60 POINT COMMUTATOR RING B
    - WATER SEALS
    - METERING MONITORS
    - DOOR SEPARATIONS
    - PLATEN POSITIONS
    - YAW POSITION
    - CURRENT MONITORS
    - VOLTAGE MONITORS

# ELECTRICAL DESIGN



TAPE RECORDER

J-3 DESIGN REVIEW

# ELECTRICAL DESIGN

## TAPE RECORDER SYSTEM

- OPERATED FROM INSTRUMENT OPERATE COMMAND
- RECORDS DATA FROM TWO MAIN SOURCES:
  - DIGITAL DATA #1
    - OUTPUT OF AN A/D MULTIPLEXER
    - A/D MULTIPLEXER INPUTS:
      - AGENA THRUST MONITOR DATA (12 CHANNELS)
      - SERIAL TIME WORD FROM CLOCK (2 CHANNELS)
      - TWO ELECTRONIC COMMUTATOR OUTPUTS (2 CHANNELS)
  - DIGITAL DATA #2
    - TWO PULSE STRETCHER OUTPUTS
    - PULSE STRETCHERS DRIVEN BY CLOCK SYNC OUTPUTS
    - CLOCK SYN. OUTPUTS DRIVEN BY MAIN INSTRUMENTS CENTER OF FORMAT PULSES
- DIGITAL CLOCK SYNC
  - TIME BASE FROM A/D CONVERTER
  - 10 KC PULSE RECORDED ON BOTH CHANNELS



ELECTRICAL DESIGN

ENGINEERING ANALYSIS

FAILURE MODES ARE DISCUSSED IN DETAIL FOR EACH OF THE MAJOR SUBSYSTEMS IN THE FOLLOWING DOCUMENTS:

- ENGINEERING ANALYSIS REPORT - T/M SUBSYSTEM T9-6-041
- ENGINEERING ANALYSIS REPORT - EXPOSURE CONTROL SUBSYSTEM T9-6-037
- ENGINEERING ANALYSIS REPORT - COMMAND SUBSYSTEM T9-6-042
- ENGINEERING ANALYSIS REPORT - J-3 PYROTECHNIC SUBSYSTEM T9-6-036
- ENGINEERING ANALYSIS REPORT - PMU SUBSYSTEM T9-6-039
- ENGINEERING ANALYSIS REPORT - DIGITAL TAPE RECORDER SUBSYSTEM T9-6-038
- ENGINEERING ANALYSIS REPORT - IMC SUBSYSTEM T9-6-034
- ENGINEERING ANALYSIS REPORT - SLP DATA CONDITIONER T9-6-035
- ENGINEERING ANALYSIS REPORT - POWER SUBSYSTEM T9-6-040

J-3 DESIGN REVIEW

~~TOP SECRET~~

NO

# ELECTRICAL DESIGN

## BIBLIOGRAPHY

### A/P INTERFACE DOCUMENTS

T3-5-019 ELECTRICAL INTERFACE SPECIFICATION FOR THE J-3 CONSTANT ROTATOR SYSTEM

T3-5-020 ELECTRICAL INTERFACE SPECIFICATION FOR THE J-3 SRV SYSTEM

T3-5-021 ELECTRICAL INTERFACE SPECIFICATION FOR THE J2-J3 DISIC SYSTEM

T3-5-023 ELECTRICAL INTERFACE SPECIFICATION FOR ADVANCED PROJECTS PAYLOAD AND  AGENA ORBITAL VEHICLE

### A/P DESIGN CONTROL SPECIFICATIONS

T3-5-028 ADVANCED PROJECTS DESIGN CONTROL SPECIFICATION FOR J-3 SYSTEM

ELECTRICAL DESIGN

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J-3 DESIGN REVIEW

NO.